ABSTRACT OF THE DISCLOSURE

Please amend the abstract as follows:

A magnetic memory includes a plurality of variable resistors arrayed as memory elements in a matrix, a plurality of bit lines each of which is arranged on each row of the matrix and connected to one terminal of each ~~of~~ variable resistor ~~resistors~~ belonging to the same row, a read circuit which detects the resistance values of the variable resistors based on currents flowing through the bit lines, and load elements connected to ~~arranged on~~ the bit lines independently of and in parallel with the memory elements.